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Atty. Docket No.: M-15210 US

October 3, 2003

Mail Stop Patent Application Director of USPTO P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Yi Ding

Title: Nonvolatile Memory Fabrication Methods Comprising Lateral Recessing of Dielectric

Sidewalls At Substrate Isolation Regions

X Return Receipt Postcard

X This Transmittal Letter (in duplicate)

8 page(s) Specification

3 page(s) Claims

1 page Abstract

8 Sheet(s) of Drawings

2 page(s) Declaration For Patent Application and Power of Attorney

page Recordation Form Cover Sheet (in duplicate)

1 page Assignment

CLAIMS AS FILED

For Total Claims	Number <u>Filed</u> 13	-20	=	Number Extra 0	х	<u>Rate</u> \$18.00	=	\$ Basic Fee <u>770.00</u> 0.00
Independent	1	-3	=	0	X	\$86.00	=	\$ 0.00
Claims								
Fee of for the first filing of one or more								\$
multiple dependent claims per application								
Fee for Request for Extension of Time								\$
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The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-2257.

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Respectfully submitted,

Michael Shouker

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